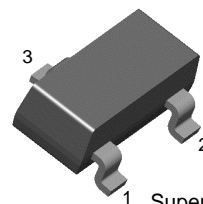


FSB6726

PNP General Purpose Amplifier

- This device designed for general purpose medium power amplifiers and switches requiring collector currents to 1.0A.
- Sourced from process 77.



1 SuperSOT™-3
1. Base 2. Emitter 3. Collector

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CEO}	Collector-Emitter Voltage	30	V
V_{CBO}	Collector-Base Voltage	40	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current - Continuous	1.5	A
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 ~ 150	$^\circ\text{C}$

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

1. These ratings are based on a maximum junction temperature of 150°C
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Max.	Units
Off Characteristics					
BV_{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 10\text{mA}$	30		V
BV_{CBO}	Collector-Base Breakdown Voltage	$I_C = 100\mu\text{A}$	40		V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_E = 100\mu\text{A}$	5		V
I_{CBO}	Collector Cutoff Current	$V_{CB} = 40\text{V}$		100	nA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = 5\text{V}$		100	nA
On Characteristics *					
h_{FE}	DC Current Gain	$I_C = 100\text{mA}, V_{CE} = 1\text{V}$ $I_C = 1\text{A}, V_{CE} = 1\text{V}$	60 50	250	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 1\text{A}, I_B = 100\text{mA}$		500	mV
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C = 1\text{A}, V_{CE} = 1\text{V}$		1.2	V
Small Signal Characteristics					
C_{cb}	Collector-Base Capacitance	$V_{CB} = 10\text{V}, f = 1\text{MHz}$		30	pF
h_{fe}	Small Signal current Gain	$I_C = 50\text{mA}, V_{CE} = 10\text{V}, f = 20\text{MHz}$	2.5	25	

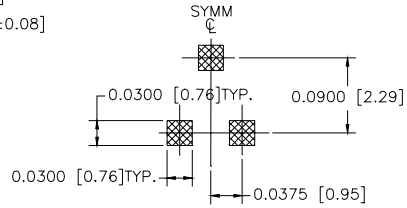
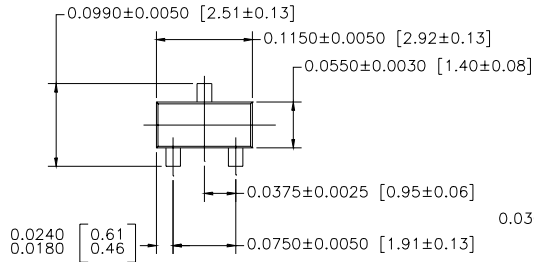
*Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2.0\%$

Thermal Characteristics

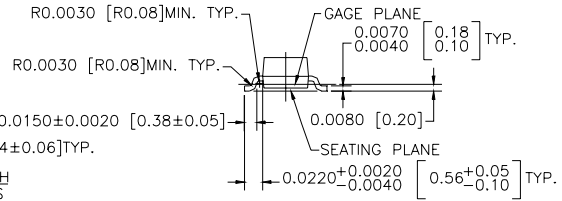
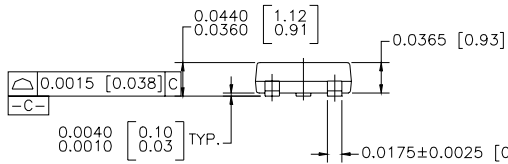
Symbol	Parameter	Max.	Units
P_D	Total Device Dissipation	500	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	250	$^\circ\text{C}$

Package Dimensions

SuperSOT™-3



LAND PATTERN RECOMMENDATION



CONTROLLING DIMENSION IS INCH
VALUES IN [] ARE MILLIMETERS

- NOTES : UNLESS OTHERWISE SPECIFIED SUPER SOT , 3 LEADS
- STANDARD LEAD FINISH TO BE 150 MICRONS / 3.81 MICROMETERS MINIMUM TIN/LEAD (SOLDER) ON COPPER.
 - NO JEDEC REGISTRATION AS OF DEC. 1995.

Dimensions in Millimeters

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PRODUCT STATUS DEFINITIONS

Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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