



# 2SK3283

## Load S/W Applications

### Preliminary

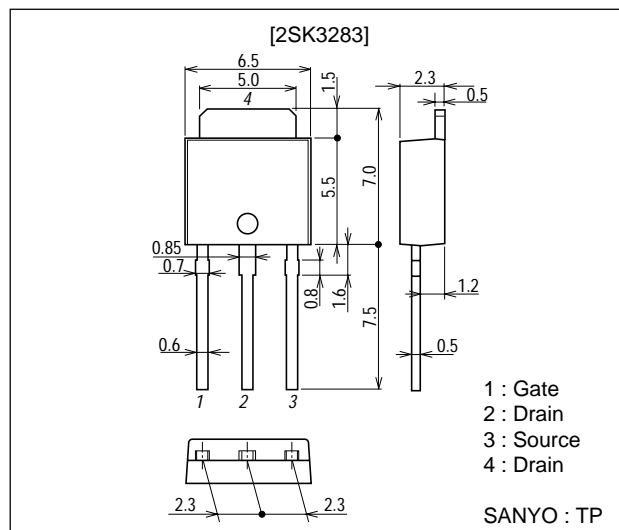
#### Features

- Low ON resistance.
- 4V-drive.

#### Package Dimensions

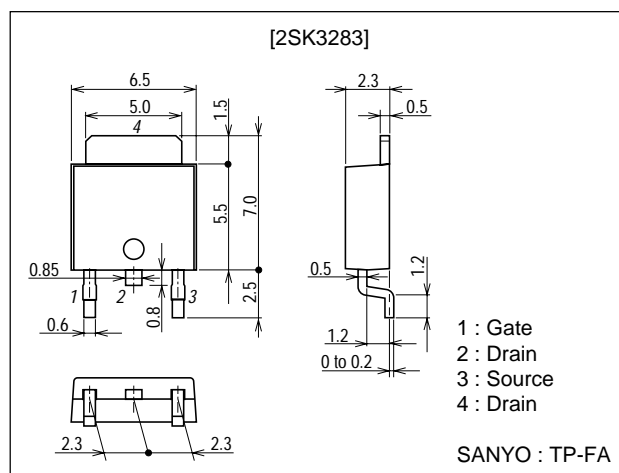
unit : mm

2083B



unit : mm

2092B



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## Specifications

### Absolute Maximum Ratings at Ta=25°C

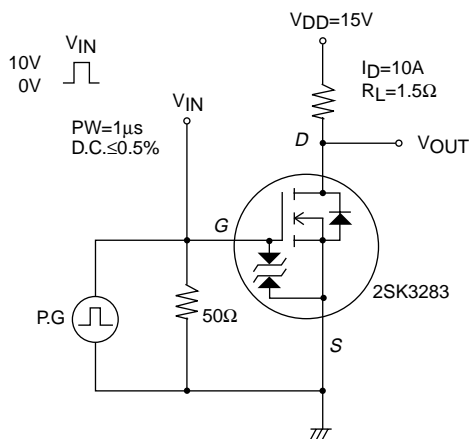
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		30	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±20	V
Drain Current (DC)	I <sub>D</sub>		15	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	45	A
Allowable Power Dissipation	P <sub>D</sub>		1	W
		T <sub>c</sub> =25°C	20	W
Channel Temperature	T <sub>ch</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	30			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0			1	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0			±10	μA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.0		2.4	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =10A	11.2	16		S
Static Drain-to-Source On-State Resistance	R <sub>DS(on)1</sub>	I <sub>D</sub> =10A, V <sub>GS</sub> =10V		17	23	mΩ
	R <sub>DS(on)2</sub>	I <sub>D</sub> =4A, V <sub>GS</sub> =4V		25	35	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =10V, f=1MHz		1550		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =10V, f=1MHz		350		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	V <sub>DS</sub> =10V, f=1MHz		220		pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit		12		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		300		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>	See specified Test Circuit		105		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		100		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =15A		40		nC
Gate-to-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =15A		5		nC
Gate-to-Drain "Miller" Charge	Q <sub>gd</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =15A		7		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =15A, V <sub>GS</sub> =0		0.92	1.2	V

Marking : K3283

### Switching Time Test Circuit



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