

SANYO	No.1626C	2SC3642
		NPN Triple Diffused Planar Silicon Transistor Ultrahigh-Definition Display Horizontal Deflection Output Applications

Features

- High reliability (Adoption of HVP process).
- High speed.
- High breakdown voltage.
- Adoption of MBIT process.

Absolute Maximum Ratings at Ta = 25°C

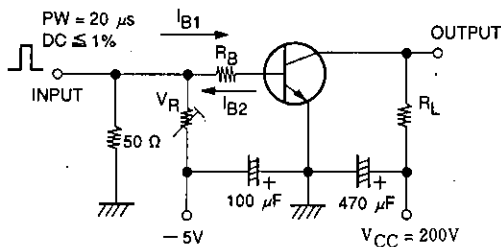
			unit
Collector-to-Base Voltage	V _{CB0}	1200	V
Collector-to-Emitter Voltage	V _{CEO}	800	V
Emitter-to-Base Voltage	V _{EBO}	7	V
Collector Current	I _C	6	A
Collector Current (Pulse)	I _{CP}	12	A
Collector Dissipation	P _C	100	W
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

T_c = 25°C

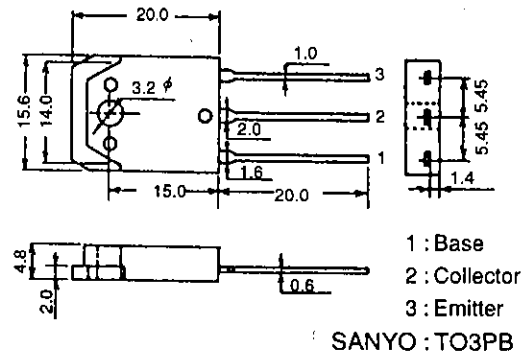
Electrical Characteristics at Ta = 25°C

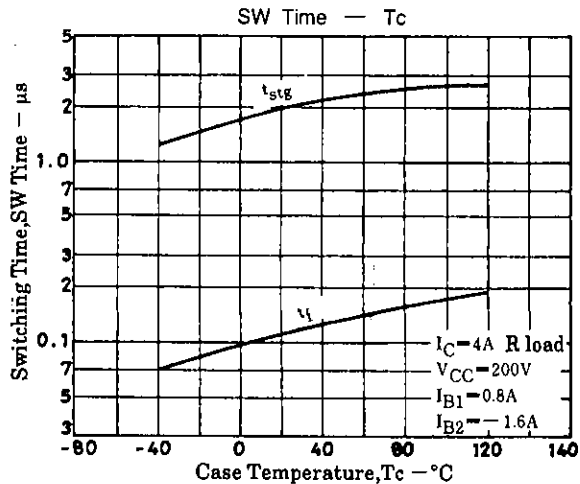
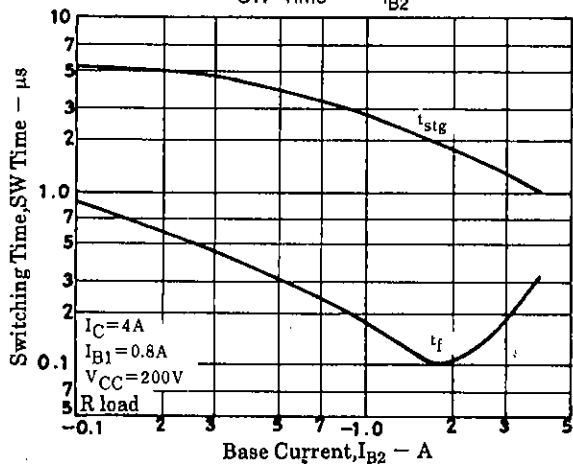
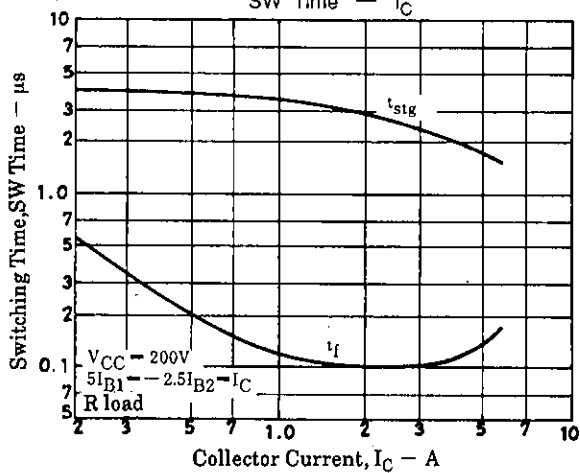
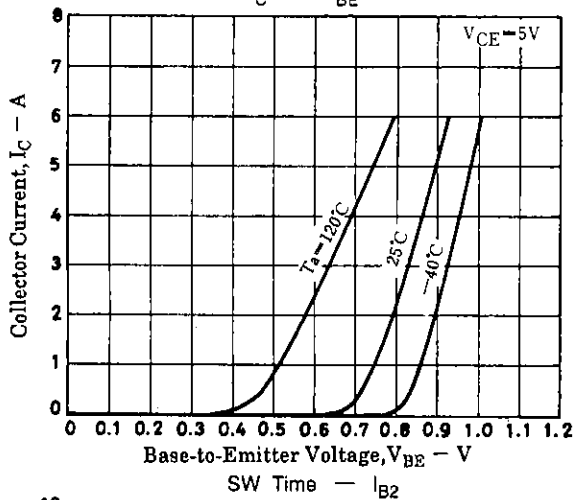
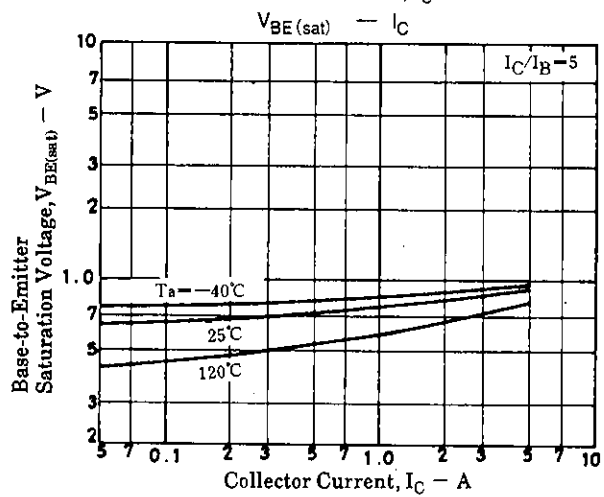
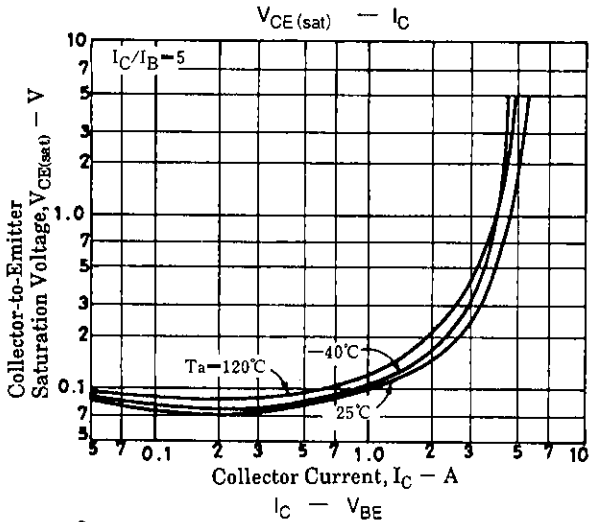
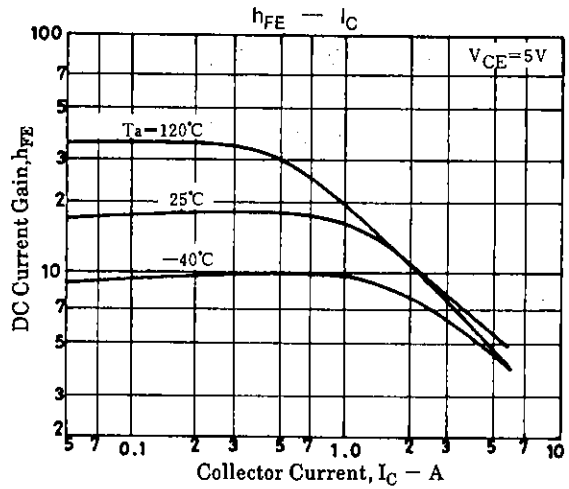
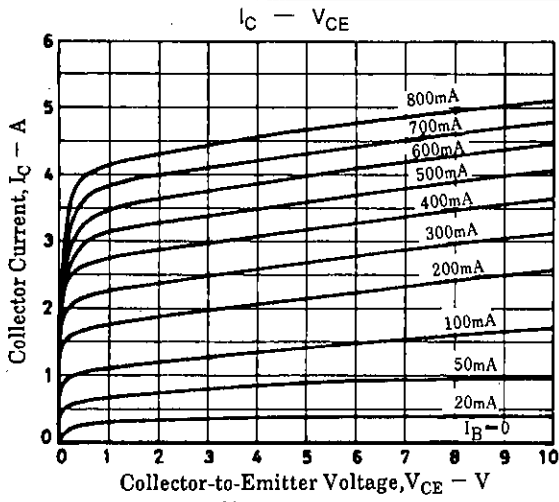
			min	typ	max	unit
Collector Cutoff Current	I _{CBO}	V _{CB} = 800V, I _E = 0			10	μA
	I _{CES}	V _{CE} = 1200V, R _{BE} = 0			0.5	mA
C-E Sustaining Voltage	V _{CEO(sus)}	I _C = 100mA, I _B = 0	800			V
Emitter Cutoff Current	I _{EBO}	V _{EB} = 5V, I _C = 0			1	mA
C-E Saturation Voltage	V _{CE(sat)}	I _C = 4A, I _B = 0.8A			5	V
B-E Saturation Voltage	V _{BE(sat)}	I _C = 4A, I _B = 0.8A			1.5	V
DC Current Gain	h _{FE}	V _{CE} = 5V, I _C = 0.8A	8			
Storage Time	t _{stg}	I _C = 4A, I _{B1} = 0.8A, I _{B2} = -1.6A			3.0	μs
Fall Time	t _f	I _C = 4A, I _{B1} = 0.8A, I _{B2} = -1.6A		0.1	0.2	μs

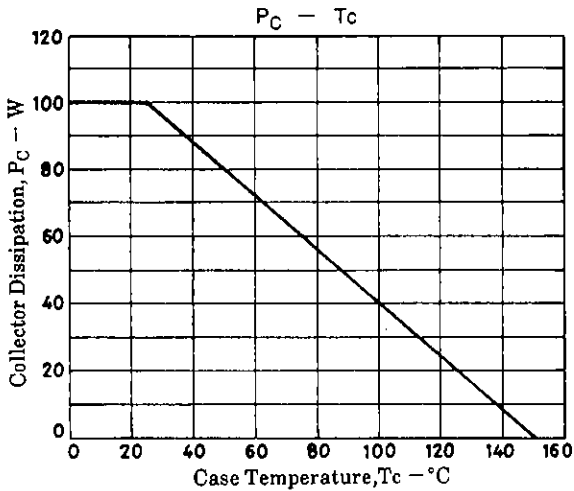
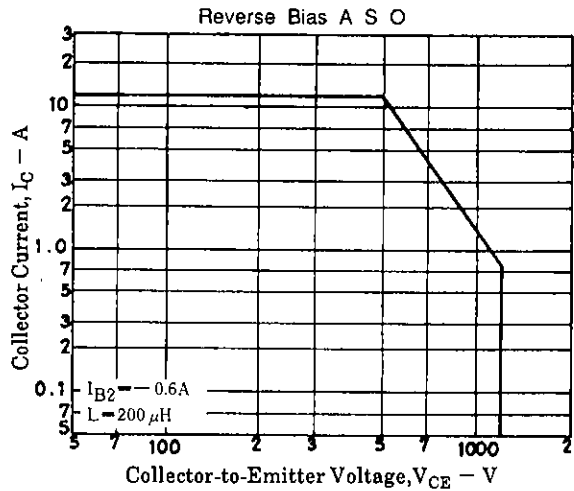
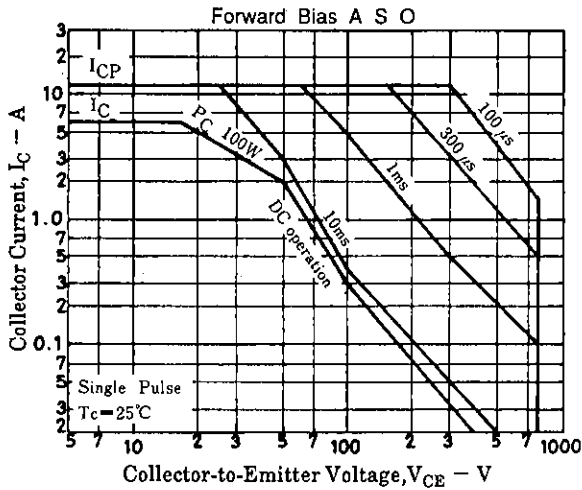
Switching Time Test Circuit



Package Dimensions 2022A
(unit: mm)







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