

**SCHOTTKY RECTIFIER**  
HIGH EFFICIENCY SERIES

**16SCYQ060C**

16 Amp, 60V

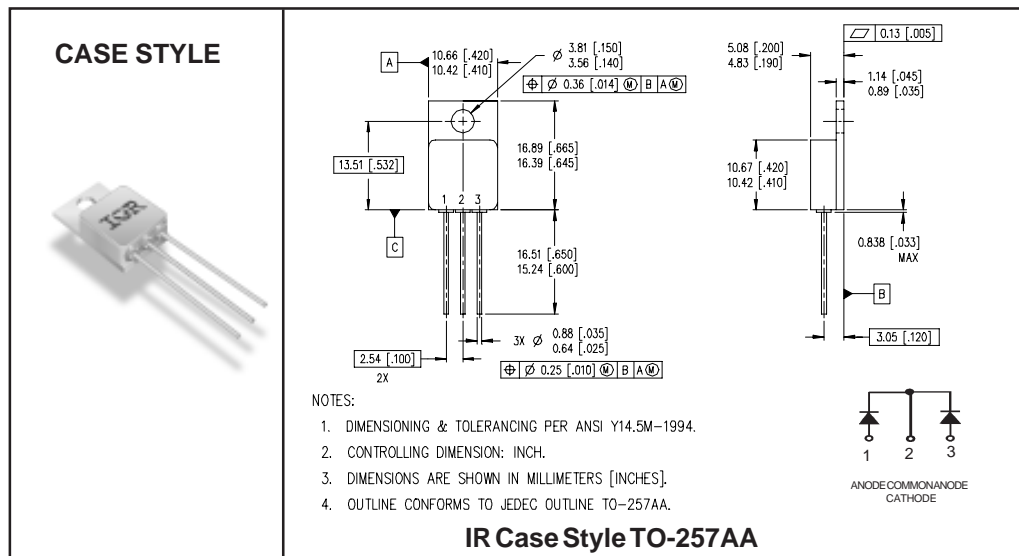
**Major Ratings and Characteristics**

Characteristics	16SCYQ060C	Units
$I_{F(AV)}$	16	A
$V_{RRM}$ (Per Leg)	60	V
$I_{FSM}$ @ $t_p = 8.3ms$ half-sine (Per Leg)	125	A
$V_F$ @ 16Apk, $T_J = 125^\circ C$ (Per Leg)	0.79	V
$T_J, T_{stg}$ Operating and storage	-55 to 150	$^\circ C$

**Description/Features**

The 16SCYQ060C center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of hi-rel environments. It is packaged in the hermetic isolated TO-257AA package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

- Hermetically Sealed
- Ceramic Eyelets
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long term Reliability
- Lightweight



**Voltage Ratings**

Part number	16SCYQ060C
V <sub>R</sub> Max. DC Reverse Voltage (V) (Per Leg)	60
V <sub>RWM</sub> Max. Working Peak Reverse Voltage (V) (Per Leg)	

**Absolute Maximum Ratings**

Parameters	Limits	Units	Conditions
I <sub>F(AV)</sub> Max. Average Forward Current See Fig. 5	16	A	50% duty cycle @ T <sub>C</sub> = 127°C, square waveform
I <sub>FSM</sub> Max. Peak One Cycle Non - Repetitive Surge Current (Per Leg)	125	A	@ t <sub>p</sub> = 8.3 ms half-sine

**Electrical Specifications**

Parameters	Limits	Units	Conditions
V <sub>FM</sub> Max. Forward Voltage Drop (Per Leg) See Fig. 1①	0.63	V	@ 8.0A T <sub>J</sub> = -55°C
	0.74	V	@ 16A
	0.58	V	@ 8.0A T <sub>J</sub> = 25°C
	0.77	V	@ 16A
	0.55	V	@ 8.0A T <sub>J</sub> = 125°C
	0.79	V	@ 16A
I <sub>RM</sub> Max. Reverse Leakage Current (Per Leg) See Fig. 2①	0.68	mA	T <sub>J</sub> = 25°C
	84	mA	T <sub>J</sub> = 100°C
	150	mA	T <sub>J</sub> = 125°C
C <sub>T</sub> Max. Junction Capacitance (Per Leg)	700	pF	V <sub>R</sub> = 5V <sub>DC</sub> ( 1MHz, 25°C)
L <sub>S</sub> Typical Series Inductance (Per Leg)	6.9	nH	Measured from anode lead to cathode lead 6mm ( 0.025 in.) from package

**Thermal-Mechanical Specifications**

Parameters	Limits	Units	Conditions
T <sub>J</sub> Max. Junction Temperature Range	-55 to 150	°C	
T <sub>stg</sub> Max. Storage Temperature Range	-55 to 150	°C	
R <sub>thJC</sub> Max. Thermal Resistance, Junction to Case (Per Leg)	2.0	°C/W	DC operation See Fig. 4
R <sub>thJC</sub> Max. Thermal Resistance, Junction to Case (Per Package)	1.0	°C/W	DC operation
wt Weight (Typical)	4.3	g	
Die Size (Typical)	105X125	mils	
Case Style	T0-257AA		

① Pulse Width &lt; 300μs, Duty Cycle &lt; 2%

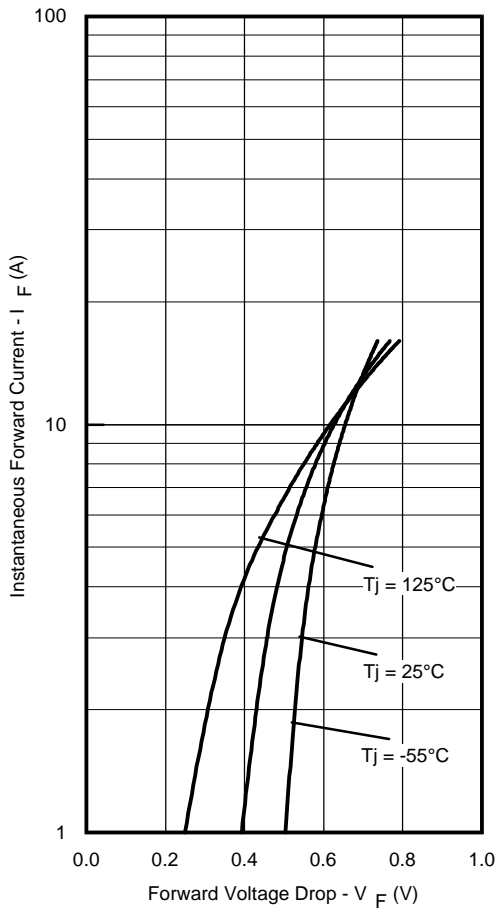


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

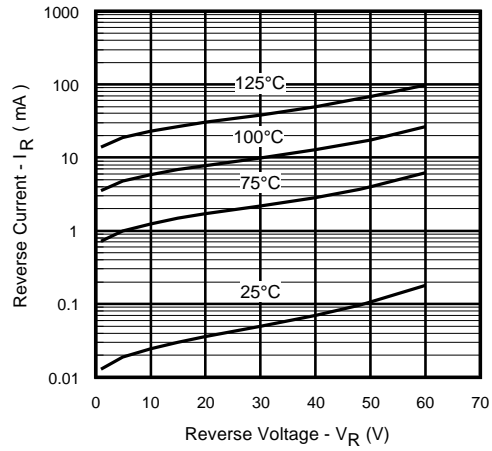


Fig. 2 - Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

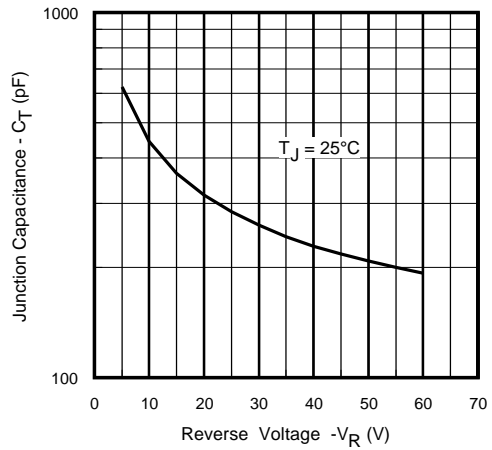


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

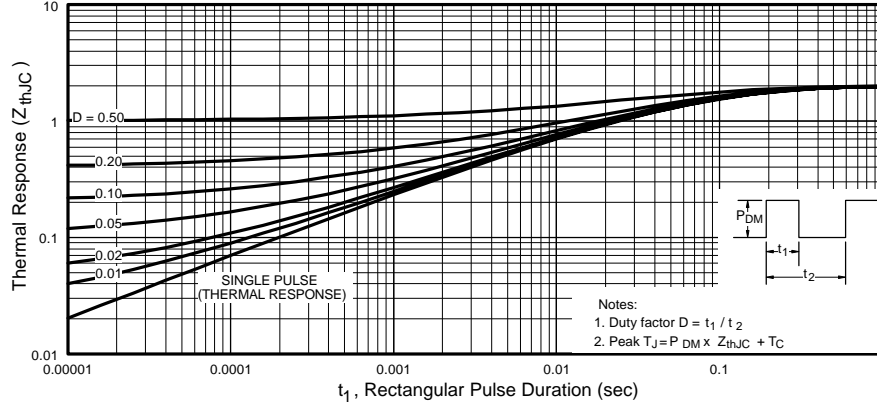


Fig. 4 - Max. Thermal Impedance  $Z_{thJC}$  Characteristics (Per Leg)

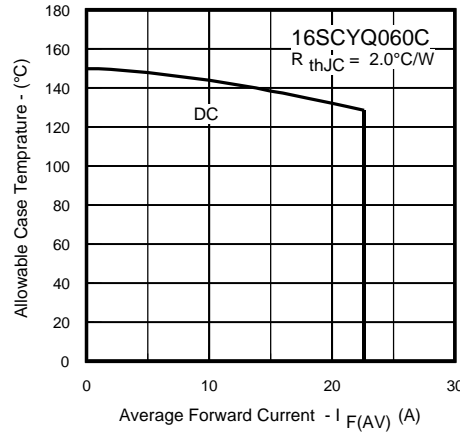


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)