

FULLY PROTECTED H-BRIDGE FOR D.C. MOTOR

The IR3220S is a fully protected dual high side switch I.C that integrates an H-bridge motor controller with two very efficient high side MOSFETs in a single 20-pin package. The IR3220S combines with the two low side IRF7484Q MOSFETs as few as 10 external passive components to provide a complete, fully operational and fully protected H-bridge control actuator with forward, reverse, braking and non-braking modes **without the need of a micro-controller.**

Functional Description

The high side switches provide the direction capability and the H-bridge protection. The low side MOSFETs bring the flexibility by offering the high frequency switching ability. Therefore, crude start-up of the motor is avoided and replaced by a smooth and stress-less speed ramp-up.

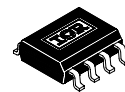
The IR3220S features shoot-through protection for each leg, H-bridge logic control, soft-start sequence and over-current / over-temperature shutdown protections. Two input signals (IN1 & IN2) select the operating modes while the PWM soft-start sequence cycles the corresponding active low side MOSFET in order to limit the motor in-rush current. The soft-start sequence is programmed by an RC time constant and reset itself automatically. Thanks to the inner PWM oscillator, the IR3220S can also be the final stage of an overall torque or speed loop. If needed, an external clock may force the H-bridge switching operation. This can be combined with low frequency PWM operation through the IN1(2) inputs.

The IR3220S is a Co-pack IPS product offering very low $R_{ds(on)}$ and a high level of functionality and protection. Its open architecture and programmability helps the designer to optimize each motor drive upon the application requirements at a very low cost. For automotive actuators, the motor is kept shorted even during the low consumption sleep mode. Shoot-through protection, over-temperature & over-current shutdowns, self-adaptive dead-time and PWM circuitries are described in details in the AN 1032 Application Note. A general purpose method to help rating the soft-start sequence as well as layout and thermal considerations are also covered. Finally, a 6A DC motor actuator with a PCB size down to 1 Inch² is suggested in the document.

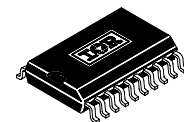
Features

- Programmable PWM In-rush Current Limitation (e.g 18A)
- 6 A Continuous Current Capability without Heat Sink (2 x 13 mΩ)
- Over-Temperature (165 °C) and Over-Current (30A) Protections
- 20 kHz PWM Oscillator Embedded
- Low & High Frequency Switching Operation (self adaptive dead-time)
- Easy Speed / Torque Control (analog duty cycle input)
- Braking / Non-Braking Modes
- Sleep Mode (braking) for Automotive Actuator

Packages

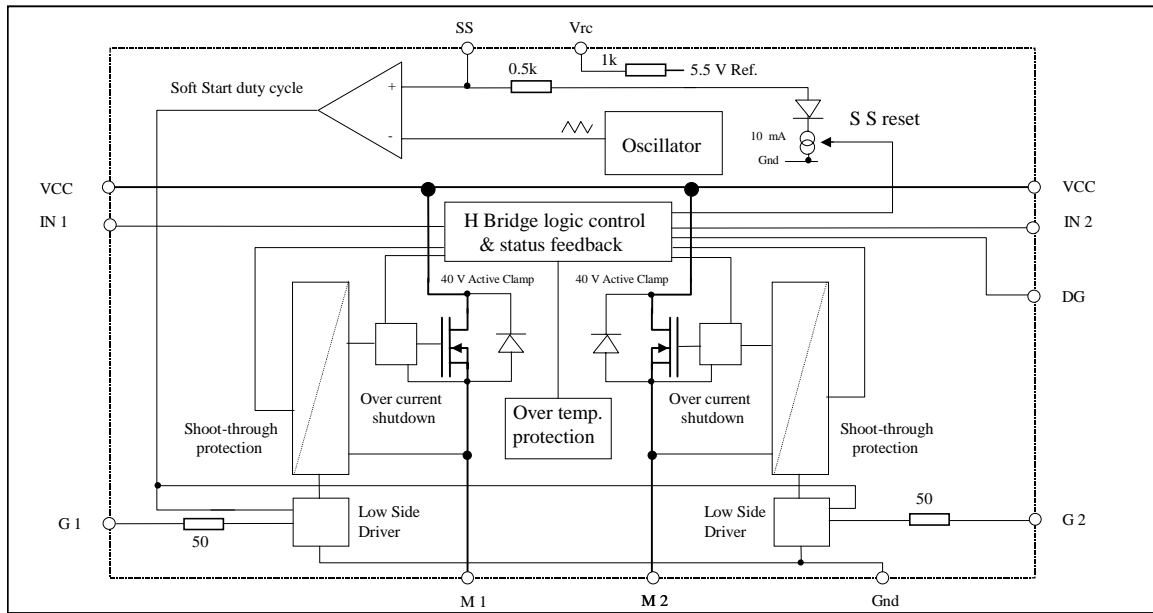


8-Lead SOIC
 IRF7484Q



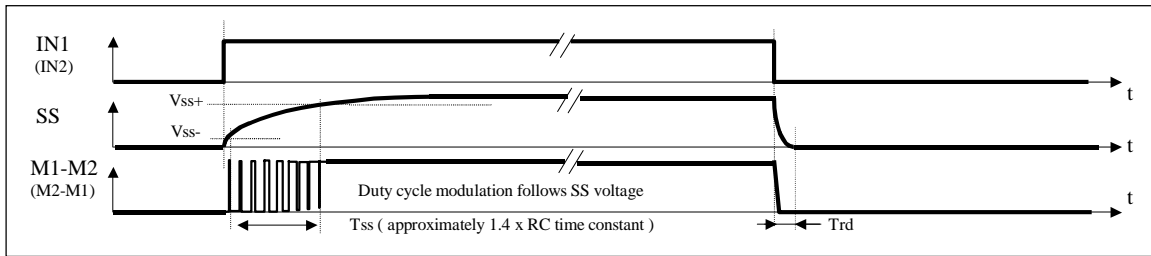
20-Lead SOIC
 (wide body)

Functional Block Diagram (see AN-1032 for a detailed description of each block)



Thanks to the self-adaptive dead-time circuitry, the low side MOSFET of each leg is driven in the opposite phase of the high side one without any conflict. Thus, the single IN1 signal turns on the leg M1 (and IN2, the output M2). Consequently, when both IN1 and IN2 are low, the quiescent state of the H-bridge is the Braking Mode (the two low side MOSFETs on). The over-temperature circuitry and the two over-current protections (one per leg) protect the IC and flag the DG pin. The thermal shutdown also covers the body diode overheating. Fault conditions are reset by cycling the corresponding IN1(2) input. Each leg appears independent so that the PWM soft-start management is greatly simplified and makes the 20kHz oscillator block almost a separate function. The positive input of the PWM comparator is accessible on the SS pin. An external analog voltage or a RC network can either drive the duty cycle. It has to be said that a clock signal (< 20 kHz) applied on this input will directly drive the low side MOSFETs. A 5V voltage source is embedded in the I.C (switched off while in the sleep mode) so that no additional power supply is needed for the soft-start RC time constant. Its capacitor is discharged through the ' SS reset ' circuitry every time IN1 equals IN2. Thus, the soft-start sequence is ready to operate whichever the formerly braking mode was.

Soft-Start Sequence



Truth Table

| IN1 | IN2 | MODES | DG | HS1 | LSS1 | HS2 | LSS2 | SS reset |
|-----|-----|---|----|-----|------|-----|------|----------|
| L | L | Stand-by with braking - sleep mode** | H | OFF | ON | OFF | ON | ON |
| L | H | Forward rotation (normal operation) | H | OFF | ON* | ON | OFF | OFF |
| L | H | Forward rotation (protection triggered) | L | OFF | ON* | OFF | OFF | OFF |
| H | L | Reverse rotation (normal operation) | H | ON | OFF | OFF | ON* | OFF |
| H | L | Reverse rotation (protection triggered) | L | OFF | OFF | OFF | ON* | OFF |
| H | H | Stand-by without braking | H | OFF | OFF | OFF | OFF | ON |

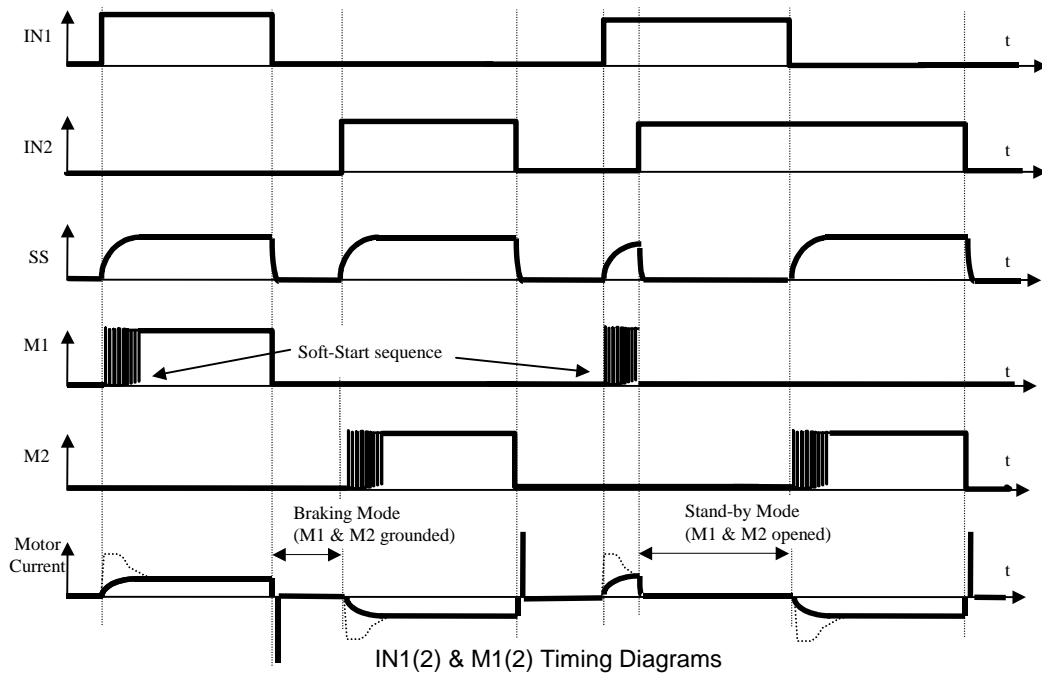
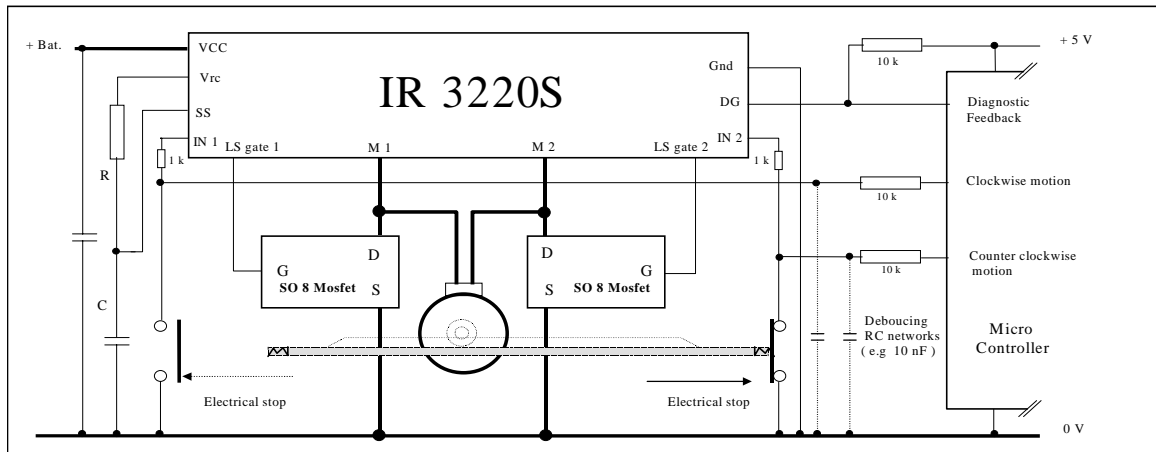
* During Soft-start sequence, the low side part is switching.

** Protections are reset in this mode

The IR 3220S over-current is set at 30A which is low enough to protect the whole application. The soft-start RC time constant has to be designed in order to keep the maximum in-rush current below the I shutdown (application worst case - see AN 1032). The total switching sequence is about 1.4 times the RC time constant. A smoother start-up is even achievable by slightly increasing the RC values. However, the soft-start sequence should remain short enough not to trip the over-temperature protection (Tj while free-wheeling). The truth table shows that the soft-start sequence can be interrupted at any time. But a minimum time is needed prior to any change in the direction or re-start of a new SS sequence. Actually, the capacitor of the RC network has to be discharged and the motor fully stopped first otherwise the over-current protection might trip during the next turn-on.

The protections turn off the high side MOSFETs so that no braking sequence follows the fault detection. Both IN1 and IN2 have to go low for a minimum time in order to reset the fault circuitry. When both inputs are back to the low level, the H-bridge is in the braking mode and the motor shorted. In this mode, no protection is activated and the peak current due to the braking is not monitored. After 300 ms, the I.C sleep mode is activated and the consumption is reduced down to few micro-amps. The low side gate drivers keep the gates high so the motor remains shorted. When using end switches, the I.C goes into the low consumption mode as soon as the mechanical stop are reached. When interfacing such switches directly to the IR 3220S, de-bouncing RC networks have to be implemented on the input pins in order to prevent false over-current detection.

Typical Connection

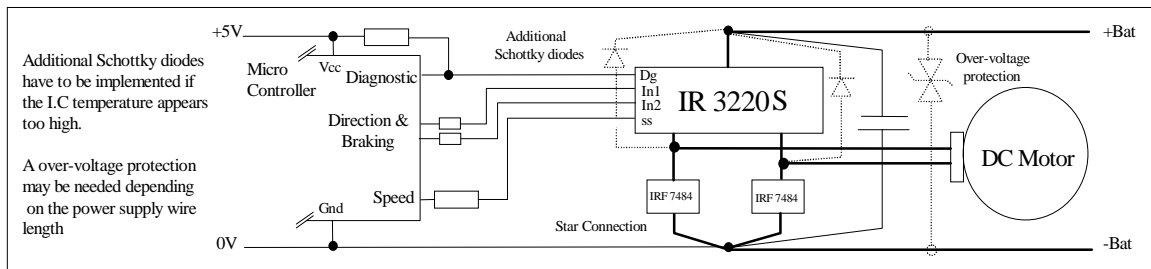


The PWM generator is based on a 3V saw-tooth oscillator. The soft-start sequence takes advantage of the RC charge profile in order to perform a smooth duty cycle variation. When the SS pin is below 1.2V, no PWM signal is sent to the low side MOSFETs. When it exceeds 4.2V, they are permanently ON. By designing the proper RC network, the start-up can either be very slow without any in-rush current or, fast and efficient by shortening the PWM sequence. In addition to the quiescent braking mode, the IR 3220S is able to open the four MOSFETs simultaneously if the mechanical load requires its natural slow-down (stand-by mode without braking). The four modes and their corresponding DC motor current profiles are summarized in the Timing Diagram.

Over-load protection is achieved thanks to the I.C temperature shutdown protection. By using the recommended part number and the proper cooling, the whole H-bridge is protected by the IR 3220S' s inner over-temperature circuitry (see AN 1032). A micro-controller is able to directly drive the PWM duty cycle by forcing a 0 to 5V voltage on the SS pin (e.g. through a 10K resistor). Thus, closing a speed or torque control loop for advanced applications becomes very easy. Since the low side MOSFETs are the only ones switching, the IR 3220S body diodes offer the freewheeling path to the motor. The power dissipated in each body diode while switching may appear high enough to trip the over-temperature protection. For permanent switching operation, external Schottky diodes should be implemented between each output (M1 & M2) and the VCC pin.

Permanent Switching Operation

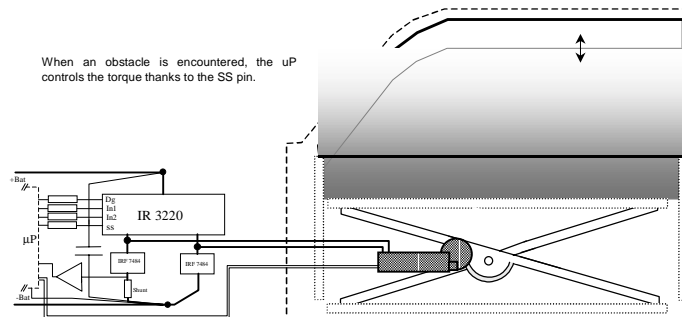
(without external RC time constant)



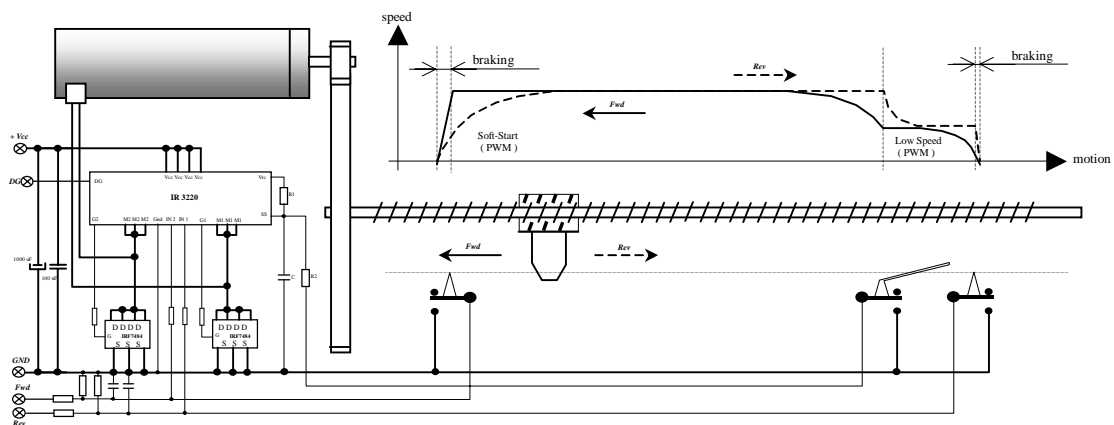
Copper plates added to the footprints will improve the cooling. However, the low side MOSFETs should always remain colder and thermally independent from the IR 3220S. The power path has to be designed carefully and shall include both a decoupling capacitor (e.g. 100 nF ceramic) and a reservoir capacitor (e.g. $C_{res} (\mu F) = I_{pk} \text{ soft-start (A)} \times 25$). The window-lifter is a good example where the IR 3220S' s PWM ability greatly enhances the application. The current is monitored thanks to a shunt and sent back to the micro-controller which takes over the torque control loop (anti-pinch function).

IR3220S

In addition, the soft-start sequence provides a smooth motion of the window. Torque or speed controls are also achievable without any micro-controller. With a few additional components, the IR 3220S can be the 'power stage' of an overall analog control loop. The SS pin is then used as the PWM duty cycle input (continuous switching operation requires high cooling capability)



For actuators, the PWM Soft Start sequence helps reduce the speed before reaching the end switches. Therefore, the braking time is very short and the actuator final position is then accurate and repeatable as shown hereunder. Mechanical stops under torque control are also possible by controlling the motor current through the PWM duty cycle.



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Gnd lead. (T_{Ambient} = 25°C unless otherwise specified). Symbols with (2) refer to M2 output.

| Symbol | Parameter | Min. | Max. | Units |
|--------------------------|---|---------------------|----------------------|-------|
| V _{m1} (2) | Maximum M1 (M2) voltage (active clamp) | V _{cc} -37 | V _{cc} +0.3 | V |
| V _{in1} (2) | Maximum IN 1 (IN 2) voltage | -0.3 | 5.5 | |
| V _{cc/gnd} | Maximum V _{cc} pin to GND pin voltage | -0.3 | 45 | |
| I _{in1} (2) | Maximum IN1 (IN 2) current | -1 | 10 | mA |
| V _{g1} (2) | Maximum Gate 1 (Gate 2) voltage | -0.3 | 7.5 | V |
| V _{ss} | Maximum SS voltage | -0.3 | 5.5 | |
| V _{rc} | Maximum V _{rc} voltage | -0.3 | 5.5 | |
| I _{rc} | Maximum output current of the V _{rc} pin | — | 1 | mA |
| V _{dg} | Maximum diagnostic output voltage | -0.3 | 5.5 | V |
| I _{dg} | Maximum diagnostic output current | -1 | 10 | mA |
| I _{sd cont.} | Diode max. permanent current (R _{th} =80°C/W) (1) (R _{th} =60°C/W) (1) | — | 2.0 3.0 | A |
| I _{sd pulsed} | Diode max. pulsed current (1) | — | 15 | |
| ESD 1 | Electrostatic discharge (human body model C=100pF, R=1500Ω) | — | 4 | kV |
| ESD 2 | Electrostatic discharge (machine model C=200pF, R=0Ω, L=10μH) | — | 0.5 | |
| PD | Maximum power dissipation (R _{th} = 80°C/W) | — | 1.5 | W |
| T _{J max.} | Max. storage & operating junction temperature | -40 | +150 | °C |
| T _L | Lead temperature (soldering 10 seconds) | — | 300 | |
| V _{cc/gnd max.} | Maximum V _{cc} to GND voltage (0.4 s - single pulse) | — | 60 | V |
| I _{g1} (2) max. | Maximum transient gate current (Ton < 5μS) | — | 100 | mA |
| I _{g1} (2) avg. | Maximum average gate current | — | 10 | |

(1) Limited by junction temperature.

Thermal Characteristics

| Symbol | Parameter | Typ. | Max. | Units |
|-------------------|---|------|------|-------|
| R _{th 1} | Junction to ambient thermal resistance (std footprint) | 80 | — | °C/W |
| R _{th 2} | Junction to ambient thermal resistance (1" sq. footprint) | 60 | — | |

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

| Symbol | Parameter | Min. | Max. | Units |
|---------------------------|--|------|------|-------|
| V _{cc} | Continuous V _{cc} voltage (2) | 8 | 28 | V |
| V _{in1} (2) | High level IN 1 (IN 2) input voltage | 4 | 5.5 | |
| V _{in1} (2) | Low level IN 1 (IN 2) input voltage | -0.3 | 0.9 | |
| I _{out} Ta=85°C | Continuous output current (Std footprint - T _j = 150°C) | — | 6.0 | A |
| I _{out} Ta=105°C | Continuous output current (Std footprint - T _j = 150°C) | — | 5.0 | |
| R _{in} | Recommended resistor in series with IN pin | 0.5 | 5 | kΩ |
| R _{dg} | Recommended pull-up resistor on DG pin | 10 | 20 | |
| R | Soft-Start resistor | 20 | 200 | |
| C | Soft-Start capacitor | 0.1 | 3.3 | μF |
| R _{gate} | Recommended gate resistor for Low Side Switch | 0 | 50 | Ω |
| L _m min. | Minimum motor inductance required | 10 | — | μH |

Static Electrical Characteristics

(T_j = 25°C, V_{cc} = 14V unless otherwise specified.)

| Symbol | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|-----------------------------|---|------|------|------|-------|---|
| R _{ds1} on | ON state resistance T _j = 25°C | — | 11 | 13 | mΩ | V _{in1,2} = 5V, I _{m1,2} = 5A |
| R _{ds2} on | ON state resistance T _j = 150°C | — | 18 | 22 | | |
| V _{cc} oper. | Functional voltage range | 5.5 | — | 35 | V | I _d = 10mA see Figs.1,2 I _d = 5A, V _{in1,2} = 0V |
| V _{clamp1} (2) | V _{cc} to M1 (M2) clamp voltage | 37 | 40 | 48 | | |
| V _{f1} (2) | Body diode 1 (2) forward voltage | — | 0.9 | — | μA | V _{m1, 2} = 0V; T _j = 25°C V _{in1(2)} = 0V, V _{cc} = 12V |
| I _{m1} (2) leakage | M1 (M2) output leakage current | — | 10 | 50 | | |
| I _{cc} off | Supply current when off (sleep mode) | — | 10 | 50 | mA | V _{in1} = 5V |
| I _{cc} on | Supply current when on | — | 8 | 12 | | |
| V _{dgl} | Low level diagnostic output voltage | — | 0.4 | — | V | I _{dg} = 1.0mA |
| I _{dg} leakage | Diagnostic output leakage current | — | — | 10 | μA | V _{dg} = 4.5V |
| V _{ih1} (2) th. | IN1 (IN2) high threshold voltage | — | 2.6 | 3.4 | V | |
| V _{il1} (2) th. | IN1 (IN2) low threshold voltage | 1.0 | 2.0 | — | | |
| I _{in1} (2) | ON state IN1 (IN2) positive current | — | 30 | 80 | μA | V _{in1, 2} = 5V |
| V _{ccuv+} | V _{cc} UVLO positive going threshold | — | 5 | — | V | |
| V _{ccuv-} | V _{cc} UVLO negative going threshold | — | 4 | — | | |
| V _{ss+} | SS high level threshold | — | 4.2 | 4.8 | | |
| V _{ss-} | SS low level threshold | 0.8 | 1.2 | — | | |
| I _{ss} leakage | SS pin leakage current | — | 0.1 | 10 | μA | V _{ss} = 5V |
| V _{rc} | Typical voltage of the V _{rc} pin | — | 5.3 | — | V | I _{rc} = 0.25mA |
| IN1(2) hys | IN1 (2) input hysteresis | 0.2 | 0.7 | 1.5 | | I _{in} = 1mA |

Switching Electrical Characteristics

$V_{CC} = 14V$, Resistive Load = 3.0Ω , $T_j = 25^\circ C$, (unless otherwise specified).

| Symbol | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|--------------------|---|------|------|------|-------|--------------------------------|
| Tdon | Turn-on delay time | — | 55 | 100 | μs | see figure 3 |
| Tr1 | Rise time to $V_{out} = V_{CC} - 5V$ | — | 3 | 30 | | |
| Tr2 | Rise time from the end of Tr1 to $V_{out} = 90\%$ of V_{CC} | — | 40 | 200 | | |
| dV/dt (on) | Turn ON dV/dt | — | 3 | — | V/μs | |
| Tdoff | Turn-off delay time | — | 30 | 80 | μs | see figure 4 |
| Tf | Fall time to $V_{out} = 10\%$ of V_{CC} | — | 16 | 50 | | |
| dV/dt (off) | Turn OFF dV/dt | — | 2 | — | V/μs | |
| IN1 (2) max. freq. | Max. frequency on IN1 (IN2) | — | 500 | — | Hz | dt=0.5 none braking mode(2) |
| Soft-Start freq. | Soft-Start oscillator frequency | 15 | 22 | 30 | kHz | |
| Ig1 (2) min. | Min. Gate 1 (Gate 2) current | 50 | 80 | — | mA | low side driver |
| Trd | Min. IN1 (2) OFF time to reset SS | — | 8.0 | — | ms | C=3.3μF, IN1 = IN2 |
| Vg1 | Gate 1 (gate 2) voltage | — | 7 | — | V | |
| Tin1 (2) | Minimum IN1 (2) ON state for operation | 200 | 350 | 550 | μs | |
| Vst | Shoot-through protection threshold | 1.1 | 2.3 | 3.3 | V | See AN-1032 |

Protection Characteristics

| Symbol | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|--------|----------------------------|------|------|------|-------|-----------------|
| Tsd | Over-temperature threshold | — | 165 | — | °C | See figure 2 |
| Isd | Over-current threshold | 24 | 30 | 38 | A | See figure 2 |
| Treset | Reset time | — | 100 | — | μs | IN1 = IN2 = 0V |

Note 1: The low side switches present sufficient cooling capability in order to have the whole H Bridge function protected by the IR3220S inner temperature sensor.

Note 2: Switching in the none braking mode consists in cycling one of the inputs while the other one is held at the high logic level.

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Lead Definitions

| | | | |
|-----|---|-----|---|
| Vcc | Positive power supply | IN1 | Logic input 1 (Leg 1 Cdt. / mode) |
| M1 | Motor 1 output (high side source - leg 1) | IN2 | Logic input 2 (Leg 2 Cdt. / mode) |
| M2 | Motor 2 output (high side source - leg 2) | Dg | Diagnostic output (open drain) |
| G1 | Gate 1 drive output (low side gate - leg 1) | Vrc | Voltage ref. output (soft-start RC) |
| G2 | Gate 2 drive output (low side gate - leg 2) | SS | RC soft-start input (the voltage on this input drives the switching duty cycle) |
| Gnd | Power supply return | | |

Lead Assignments

| | |
|---|--|
| <p>Vcc Vcc m1 m1 m1 nc g1 Gnd In1 Vrc</p> <p>Vcc Vcc m2 m2 m2 nc g2 In2 Dg SS</p> <p>20 Lead - SOIC (wide body)</p> | <p>D D D D</p> <p>S S S G</p> <p>8 Lead - SOIC</p> |
| IR3220S | IRF7484Q |
| Part Number | |

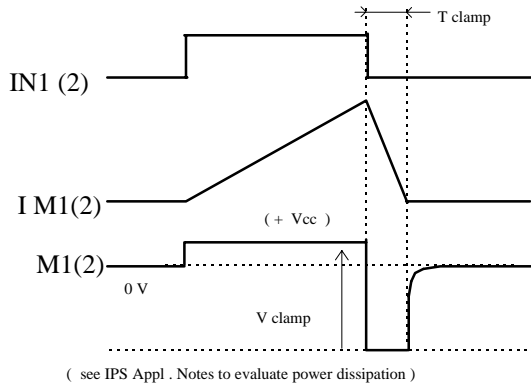


Figure 1 - Active clamp waveforms

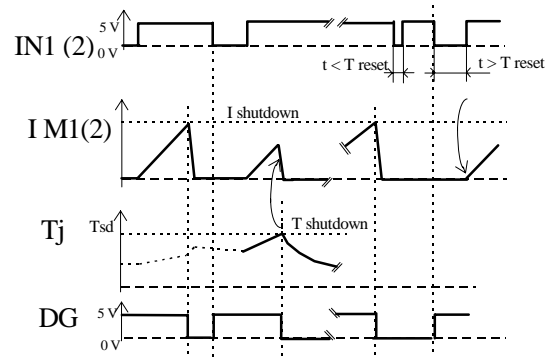


Figure 2 - Protection Timing diagram

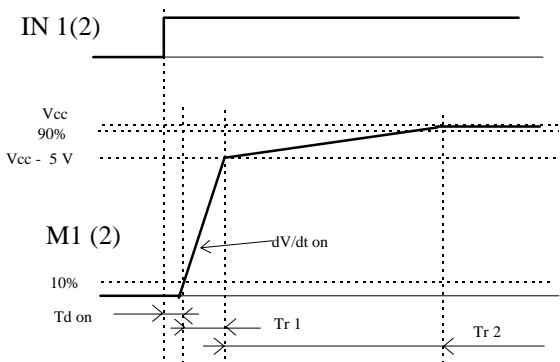


Figure 3 - Switching Time Definitions (turn-on)

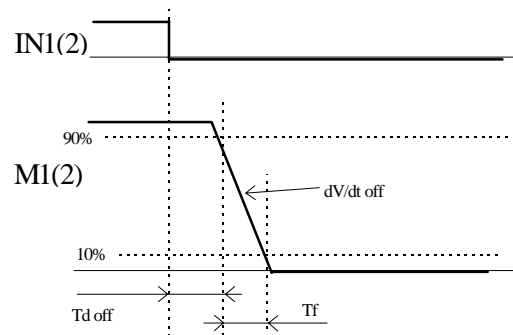


Figure 4 - Switching Time Definitions (turn-off)

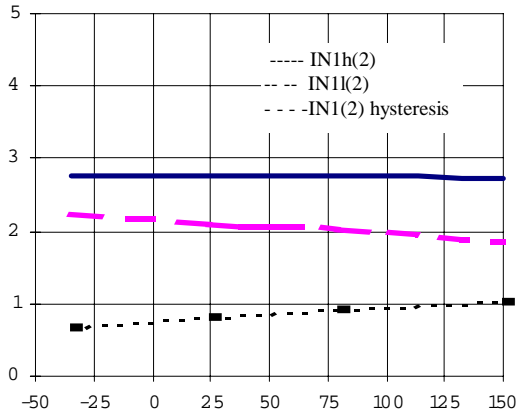


Figure 5 - IN1 (2) thresholds (V) vs Tj (°C)

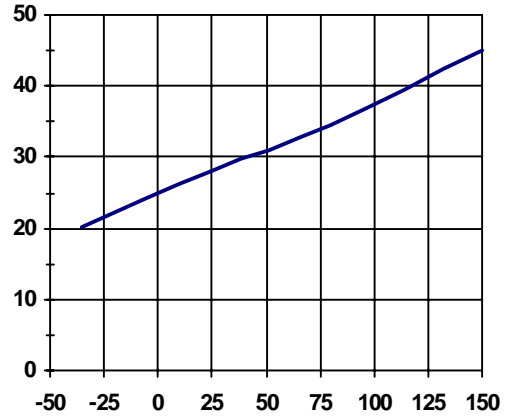


Figure 6 - IN1 (2) current (µA) vs Tj (°C)



Figure 7 - Iccoff (µA) vs Tj (°C)

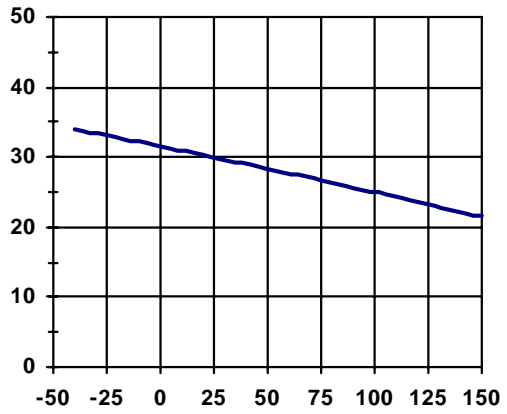


Figure 8 - Typ. I shutdown (A) vs Tj (°C)

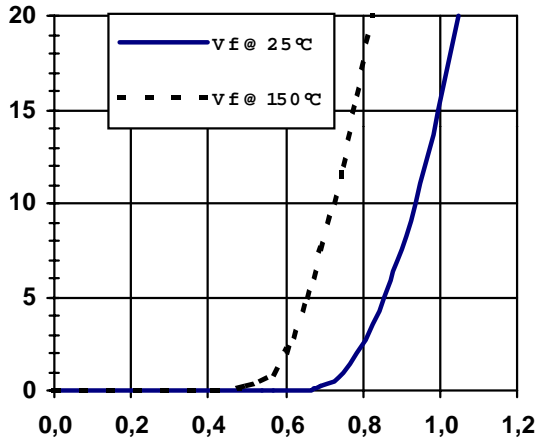


Figure 9 - Body diode : Ids (A) vs Vds (V)



Figure 10 - Rds(on) (mΩ) vs Vcc (V)

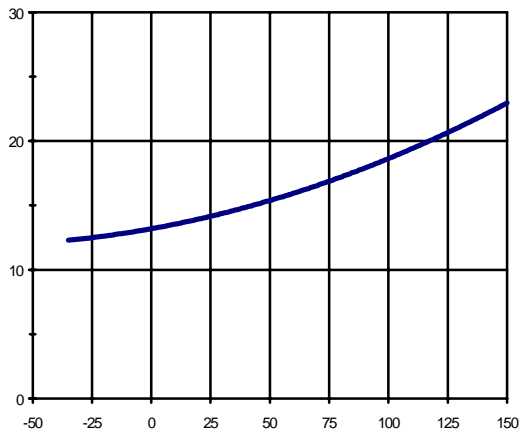


Figure 11 - Rds(on) (mΩ) vs Tj (°C)

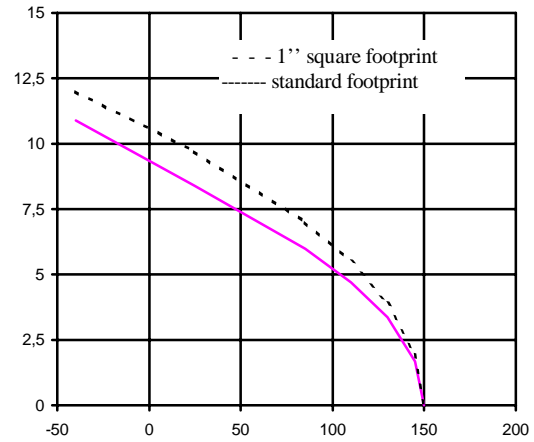


Figure 12 - Max. Cont. current (A) vs Amb. Temp. (°C)

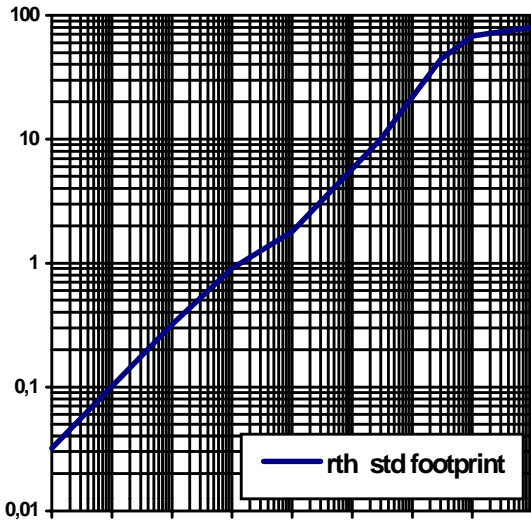


Figure 13 - Transient Rth (°C/W) vs Time (S)

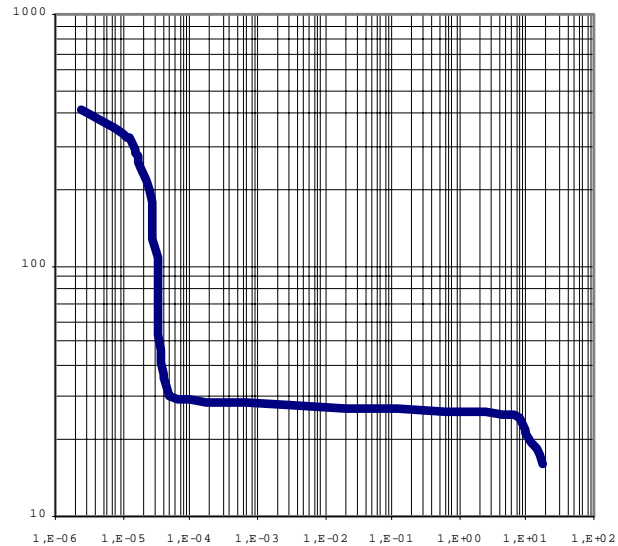


Figure 14 - Isd (A) vs Time (s)

Case Outline - 8 Lead SOIC

RECOMMENDED FOOTPRINT

| DIM | INCHES | | MILLIMETERS | |
|-----|------------|-------|-------------|------|
| | MIN | MAX | MIN | MAX |
| A | .0532 | .0688 | 1.35 | 1.75 |
| A1 | .0040 | .0098 | 0.10 | 0.25 |
| b | .014 | .018 | 0.36 | 0.46 |
| c | .0075 | .0098 | 0.19 | 0.25 |
| D | .189 | .196 | 4.80 | 4.98 |
| E | .150 | .157 | 3.81 | 3.99 |
| e | .050 BASIC | | 1.27 BASIC | |
| e1 | .025 BASIC | | 0.635 BASIC | |
| H | .2284 | .2440 | 5.80 | 6.20 |
| K | .011 | .019 | 0.28 | 0.48 |
| L | .016 | .050 | 0.41 | 1.27 |
| y | 0° | 8° | 0° | 8° |

NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- ⑤ DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 [.006].
- ⑥ DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

(MS-012AA) 01-0021 09

Case Outline

